

IEEE AP/MTT Joint Chapter, Gujarat Section

One-day Seminar

on

“ADVANCES IN COMPUTATIONAL ELECTROMAGNETICS TECHNIQUES”

July 01, 2010

Venue: Hotel Pride, Ahmedabad

Agenda

09:00 to 09:30 am	Registration
09:30 to 09:35 am	Welcome Address, Shri Dhaval Pujara Secretary, IEEE AP/MTT Joint Chapter, Gujarat Section
09.35 to 09.45 am	Opening Address, Shri Rajeev Jyoti, Chair-IEEE AP/MTT Joint Chapter, Gujarat Section
09:45 to 10:45 am	“Computational Methods for High Frequency Electromagnetics” Prof. R.P.R.C Aiyar, Principal Scientist, Centre for Research in NANO Technology & Science, IIT Mumbai
10.45 to 11.00 am	Tea Break
11.00 to 12.00 am	“Analysis of Array Antenna using MoM Technique (from Specifications to Virtual Prototyping)”, Mr. Giancarlo Guida, IDS, EM & Space lab, Italy
12.00 to 12:15 pm	Activities Planned of IEEE AP/MTT Joint Chapter, Gujarat Section By Prof. Deepak Ghodgaonkar, Vice Chair, IEEE AP/MTT Joint Chapter, Gujarat Section
12:15 to 01:00 pm	“User Experiences of Array Antenna Analysis using Antenna Design Framework : ISRO & ESA Contributions”, SAC-ISRO & ESA-ESTEC
01:00 to 01:30 pm	Lunch
01:30 to 02:00 pm	“Last Challenges for EM Simulation in Space Applications : Re-entry Vehicle, Plasma Plume from Ion Thrusters, RE/RS Analysis with Oversized Cavity Theory” Mr. Giancarlo Guida, IDS, EM & Space lab, Italy
02:00 to 03:00 pm	“Asymptotic Techniques for Analysis of Electrically Large Structures” Dr. S. B. Chakaravarthy, Head, Microwave Sensors Antenna Division, SAC, Ahmadabad
03:00 to 04:00 pm	“EM Simulation in Space Industrial Applications”, Mr. Giancarlo Guida, IDS, EM & Space lab, Italy “Advanced Numerical Techniques for Antenna Siting Analysis”, Mr. Giancarlo Guida, IDS, EM & Space lab, Italy
04:00 to 04:45 pm	ADF Demonstration
04:45 to 05:00 pm	Certificate Distribution & Vote of Thanks